p. 1

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To: Office of Initial Patent

Examination's Filing Receipt

Corrections

Fax: 703/746-9195

From: Mark C. Pickering

Date: March 22, 2004

Re: Request for Corrected Filing

Receipt for 10/729,491

Pages: 6 (including cover sheet)

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Terry Lines

Appln. No.: 10/729,491

Filed: December 4, 2003

For: METHOD FOR FORMING A

DEPLETION-MODE TRANSISTOR THAT ELIMINATES THE NEED TO SEPARATELY SET THE THRESHOLD VOLTAGE OF THE

DEPLETION-MODE TRANSISTOR

Group Art Unit: 1765

Examiner: Unknown

REQUEST TO CORRECT

FILING RECEIPT

Office of Initial Patent Examination's Filing Receipt Corrections Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

Enclosed please find a copy of the Filing Receipt (with correction to filing date indicated). Also, enclosed is a copy of the express mail airway bill EV342470581US indicating the application was mailed to U.S.P.T.O. on December 4, 2003 and a copy of the postal return receipt card showing the mailing date of December 4, 2003. Please make the necessary correction and forward the **corrected** Filing Receipt to the correspondence address indicated below.

Dated: 3-22-04

uch.Fi

Respectfully submitted

Mark C. Pickering Registration No. 36,239

Attorney for Assignee

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Request to Correct Filing Receipt

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Atty. Docket No.: 100-14320

(P04927-D01)



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33402 LAW OFFICES OF MARK C. PICKERING P.O. BOX 300 PETALUMA, CA 94953 MAR 1 2 2004

CONFIRMATION NO. 3061 FILING RECEIPT

OC0000001 2055087

Date Mailed: 03/09/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Terry Lines, Crowley, TX;

Domestic Priority data as claimed by applicant

This application is a DIV of 09/824,653 04/03/2001 PAT 6,703,670

Foreign Applications

If Required, Foreign Filing License Granted: 03/08/2004

Projected Publication Date: Request for Non-Publication Acknowledged

Non-Publication Request: Yes

Early Publication Request: No

Title

Method for forming a depletion-mode transistor that eliminates the need to separately set the threshold voltage of the depletion-mode transistor

Preliminary Class

438

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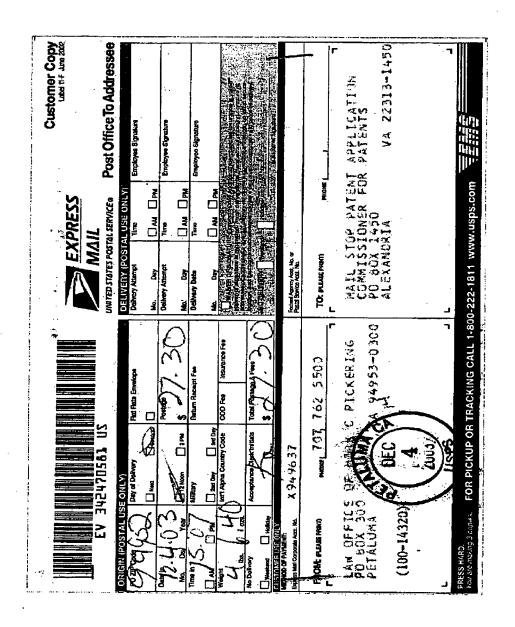
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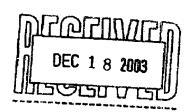
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	In the Mauer of the Application of: Terry Line	& Dunletion-Mode Transistor.
	Method for Forming	S Depletion-Mode Transistor. Due Dole: 12-23-03
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